In the Specification

Please amend the specification as follows.

Replace paragraph [0026] on page 7 with the following:

"Figure 3 illustrates mapping of stacks for both a prioritized and parameterized QoS data packet. Figure 4 illustrates process flow 1000 for transmitting data between an Ethernet network and an IEEE1394 network."

Replace paragraph [0033] on page 9 with the following:

"Figure 2 shows an exemplary adapter 230 connected to an Ethernet network 220 and to an IEEE1394 network 240. In conjunction, Figure 4 shows process flow 1000 for transmitting data between Ethernet network 220 and IEEE 1394 network 240, including the steps performed as indicated in the parenthesis below. Hardware components of adapter 230 include a first device 232 for receiving and/or transmitting data to the Ethernet side of the adapter and a second device 235 for receiving and/or transmitting data to the HiperLAN2 side of the adapter. Processor 237 is in communications with both devices (which may be for example, transceiver devices) and performs the required operations, to a) recognize that prioritized data has been received from the Ethernet (step 1100); b) determine that the level of the prioritized data (step 1200) is such that the data should be transmitted across the HiperLAN2 device in an isochronous channel (as opposed to the lower QoS asynchronous transaction) (step 1300,1400); c) establish communications over the HiperLAN2 device and set up an isochronous channel, including necessary address mapping of destination devices (step 1500); d) convert the Ethernet packet to a IEEE 1394 format packet (step 1600) and e) transmit the packet to the HiperLAN2 device on the IEEE 1394 network (step 1700). Once the isochronous channel is set up, the processor would then receive, convert and transmit all further data packets from the Ethernet transceiver until either a time out or a flag indicates that no more data remains to be transmitted to the particular address for which the isochronous channel was opened. In that case, the processor sends the appropriate message to the

Isochronous Resource Manager (IRM) on the 1394 network (which may alternatively be in the HiperLAN2 device) to signal a "teardown" of the isochronous channel."